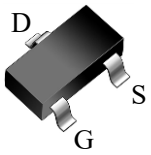
850V Depletion-Mode Power MOSFET

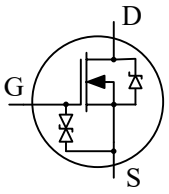
General Features

- Depletion Mode (Normally On)
- ESD Improved Capability
- Fast Switching Speed
- RoHS Compliant
- Halogen-free Available

BV_{DSX}	$R_{DS(ON)(TYP.)}$	I_{DSS}
850V	200Ω	20mA

SOT-23





Applications

- Normally-On Switches
- Converters
- Protection Circuits
- Telecommunications
- Current Regulators
- Power Supply

Ordering Information

Part Number	Package	Marking	Remark
DMZ85200E	SOT-23	85300	Halogen Free

Absolute Maximum Ratings

T_A=25℃ unless otherwise specified

Symbol	Parameter	DMZ85200E	Unit
V_{DSX}	Drain-to-Source Voltage [1]	850	V
I_D	Continuous Drain Current	20	mA
I_{DM}	Pulsed Drain Current [2]	80	
P_D	Power Dissipation	0.5	W
V_{GS}	Gate-to-Source Voltage	±20	V
T_L	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300	℃
T_J and T_{STG}	Operating and Storage Temperature Range	-55 to 150	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	DMZ85200E	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	250	K/W

Electrical Characteristics

OFF Characteristics

T_A =25℃ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
BV_{DSX}	Drain-to-Source Breakdown Voltage	850	--	--	V	V_{GS}=-10V, I_D=250μA
$I_{D(OFF)}$	Drain-to-Source Leakage Current	--	--	10	μA	V_{DS}=850V, V_{GS}=-10V
I_{GSS}	Gate-to-Source Leakage Current	--	--	±20	uA	V_{GS}=±20V, V_{DS}=0V

ON Characteristics

T_A =25℃ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
I_{DSS}	Saturated Drain-to-Source Current	20	--	--	mA	V_{GS}=0V, V_{DS}=25V
$R_{DS(ON)}$	Static Drain-to-Source On-Resistance	--	200	300	Ω	V_{GS}=0V, I_D=10mA [3]
$V_{GS(OFF)}$	Gate-to-Source Cut off Voltage	-1.5	--	-3.3	V	V_{DS}=9V, I_D=8μA
gfs	Forward Transconductance	--	--	--	S	V_{DS}=20V, I_D=10mA

Dynamic Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
C_{iss}	Input Capacitance	--	--	--	pF	V_{GS}=-10V V_{DS}=50V f=1.0MHz
C_{oss}	Output Capacitance	--	--	--		
C_{rss}	Reverse Transfer Capacitance	--	--	--		
Q_g	Total Gate Charge	--	--	--	nC	V_{GS}=-10V~5V V_{DS}=150V, I_D=10mA
Q_{gs}	Gate-to-Source Charge	--	--	--		
Q_{gd}	Gate-to-Drain (Miller) Charge	--	--	--		

Resistive Switching Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$t_{d(on)}$	Turn-on Delay Time	--	--	--	ns	V_{GS}=-10V~0V V_{DD}=50V, I_D=10mA R_G=10Ω
t_{rise}	Rise Time	--	--	--		
$t_{d(off)}$	Turn-off Delay Time	--	--	--		
t_{fall}	Fall Time	--	--	--		

Source-Drain Diode Characteristics

T_A=25℃ unless otherwise specified

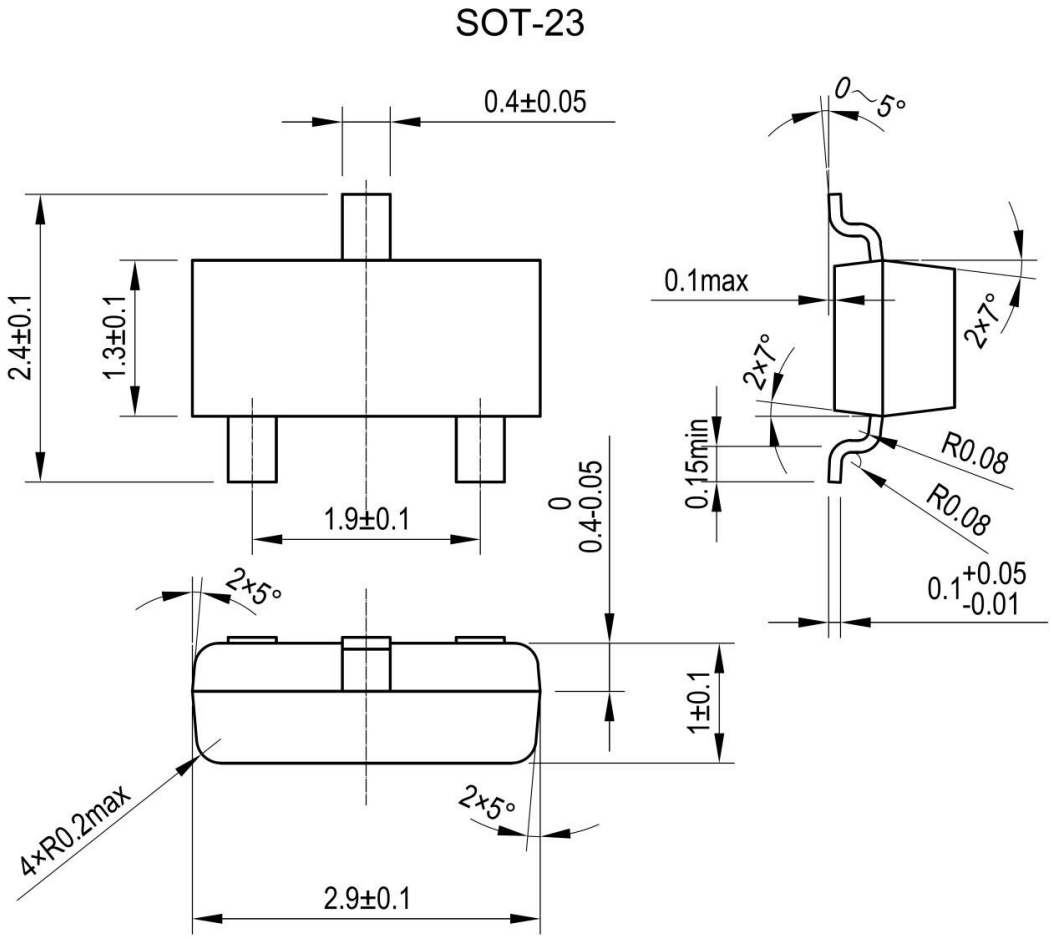
Symbol	Parameter	Min	Typ.	Max.	Units	Test Conditions
V_{SD}	Diode Forward Voltage	--	--	1.2	V	I_{SD}=10mA, V_{GS}=-10V

NOTE：

[1] T_J=+25℃ to +150℃
[2] Repetitive rating, pulse width limited by maximum junction temperature.
[3] Pulse width≤380μs; duty cycle≤2%.

Package Dimensions

SOT-23



Published by
ARK Microelectronics Co., Ltd.
ADD: D26, UESTC National Science Park No. 1 Shuangxing Avenue, Chengdu, Sichuan.

Disclaimers

ARK Microelectronics Co., Ltd. reserves the right to make change without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to ARK Microelectronics Co., Ltd's terms and conditions supplied at the time of order acknowledgement.

ARK Microelectronics Co., Ltd. warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent ARK Microelectronics Co., Ltd deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessary performed.

ARK Microelectronics Co., Ltd. does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using ARK Microelectronics Co., Ltd's components. To minimize risk, customers must provide adequate design and operating safeguards.

ARK Microelectronics Co., Ltd. does not warrant or convey any license either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in ARK Microelectronics Co., Ltd's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. ARK Microelectronics Co., Ltd is not responsible or liable for such altered documentation.

Resale of ARK Microelectronics Co., Ltd's products with statements different from or beyond the parameters stated by ARK Microelectronics Co., Ltd. for the product or service voids all express or implied warrantees for the associated ARK Microelectronics Co., Ltd's product or service and is unfair and deceptive business practice. ARK Microelectronics Co., Ltd is not responsible or liable for any such statements.

Life Support Policy:

ARK Microelectronics Co., Ltd's products are not authorized for use as critical components in life devices or systems without the expressed written approval of ARK Microelectronics Co., Ltd.

As used herein:

1. Life support devices or systems are devices or systems which:
   a. are intended for surgical implant into the human body,
   b. support or sustain life,
   c. whose failure to perform when properly used in accordance with instructions for used provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.